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Attorney Docket No.: DB000575-012

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Keeth, et al.

Examiner:

Not yet assigned

Serial No.:

09/885,217

Filed:

20 June 2001

**Art Unit:** 

Not yet assigned

**Entitled:** 

256 MEG DYNAMIC RANDOM ACCESS MEMORY

## SECOND PRELIMINARY AMENDMENT

Preliminary to the examination of the above-identified application, please amend that application as follows.

## In the figures

Please amend FIG. 36A1 to add the reference numeral 228 and a lead line for the current I1 as shown in red in the attached copy of FIG. 36A1. The requested changes are to conform the figure to the specification, particularly page 94. No new matter has been added. In the claims

Please add the following new claims.

- - 466. A memory device, comprising:

an array of memory cells;

a plurality of peripheral devices/for moving information into and out of said array of memory cells:

a plurality of voltage supplies for generating a plurality of supply voltages; and a power distribution bus for delivering said plurality of supply voltages to said array and said plurality of peripheral, devices, and wherein at least one of said voltage supplies comprises:

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